permutation and obfuscation and (one-to-one mapping) and (different)

Terms used: permutation obfuscation one to one mapping different

Found 8 of 241,625

Results 1 - 8 of 8

1 Dynamic graph-based software fingerprinting

Christian S. Collberg, Clark Thomborson, Gregg M. Townsend

October 2007 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 29 Issue 6

Publisher: ACM

Full text available: pdf(1.48 MB)

Bibliometrics: Downloads (6 Weeks): 42, Downloads (12 Months): 245, Citation Count: 0

Fingerprinting embeds a secret message into a cover message. In media fingerprinting, the secret is usually a copyright notice and the cover a digital image. Fingerprinting an object discourages intellectual property theft, or when such theft has occurred, ...

Keywords: Software piracy, software protection, watermarking

2 HIDE: an infrastructure for efficiently protecting information leakage on the address bus

Xiaotong Zhuang, Tao Zhang, Santosh Pande
December 2004 ACM SIGOPS Operating Systems Review, Volume 38 Issue 5
Publisher: ACM
Full text available: pdf(216.31 KB) Additional Information: full citation, abstract, references, cited by, index terms

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XOM-based secure processor has recently been introduced as a mechanism to provide copy and tamper resistant execution. XOM provides support for encryption/ decryption and integrity checking. However, neither XOM nor any other current approach adequately ...

Keywords: address bus leakage protection, secure processor

3 HIDE: an infrastructure for efficiently protecting information leakage on the address bus

Xiaotong Zhuang, Tao Zhang, Santosh Pande
December 2004 ACM SIGARCH Computer Architecture News, Volume 32 Issue 5
Publisher: ACM
Full text available: pdf(216.31 KB) Additional Information: full citation, abstract, references, cited by, index terms

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4 HIDE: an infrastructure for efficiently protecting information leakage on the address bus

Xiaotong Zhuang, Tao Zhang, Santosh Pande
October 2004 ASPLOS-XI: Proceedings of the 11th international conference on Architectural support for programming languages and operating systems
Publisher: ACM
Full text available: pdf(216.31 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 90, Citation Count: 7

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Keywords: address bus leakage protection, secure processor

5 HIDE: an infrastructure for efficiently protecting information leakage on the address bus

Xiaotong Zhuang, Tao Zhang, Santosh Pande
November 2004 ACM SIGPLAN Notices, Volume 39 Issue 11
Publisher: ACM
Full text available: pdf(216.31 KB) Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 90, Citation Count: 7

XOM-based secure processor has recently been introduced as a mechanism to provide copy and tamper resistant execution. XOM provides support for encryption/ decryption and integrity checking. However, neither XOM nor any other current approach adequately ...

Keywords: address bus leakage protection, secure processor

6 Drm to counter side-channel attacks?

Ryad Benadjila, Olivier Billet, Stanislas Francfort
October 2007 DRM '07: Proceedings of the 2007 ACM workshop on Digital Rights Management
Publisher: ACM
Full text available: pdf(238.48 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 100, Citation Count: 0

In the DRM setting, the attacker is a very powerful adversary, owning the software as well as the underlying hardware. This context is far different from the black-box attacker commonly considered in conventional cryptography.Therefore, cryptographers ...

Keywords: AES, DRM, side-channel attacks, white-box

7 Code protection for resource-constrained embedded devices

H. Saputra, G. Chen, R. Brooks, N. Vijaykrishnan, M. Kandemir, M. J. Irwin
July 2004 ACM SIGPLAN Notices, Volume 39 Issue 7
Publisher: ACM
Full text available: pdf(290.95 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 92, Citation Count: 0

While the machine neutral Java bytecodes are attractive for code distribution in the highly heterogeneous embedded domain, the well-documented and standardized features also make it difficult to protect these codes. In fact, there are several tools to ...

Keywords: Java security, cryptography, java byte code, mono-alphabetic, poly-alphabetic, substitution

8 Code protection for resource-constrained embedded devices

H. Saputra, G. Chen, R. Brooks, N. Vijaykrishnan, M. Kandemir, M. J. Irwin
June 2004 LCTES '04: Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems
Publisher: ACM
Full text available: pdf(290.95 KB) Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 92, Citation Count: 0

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